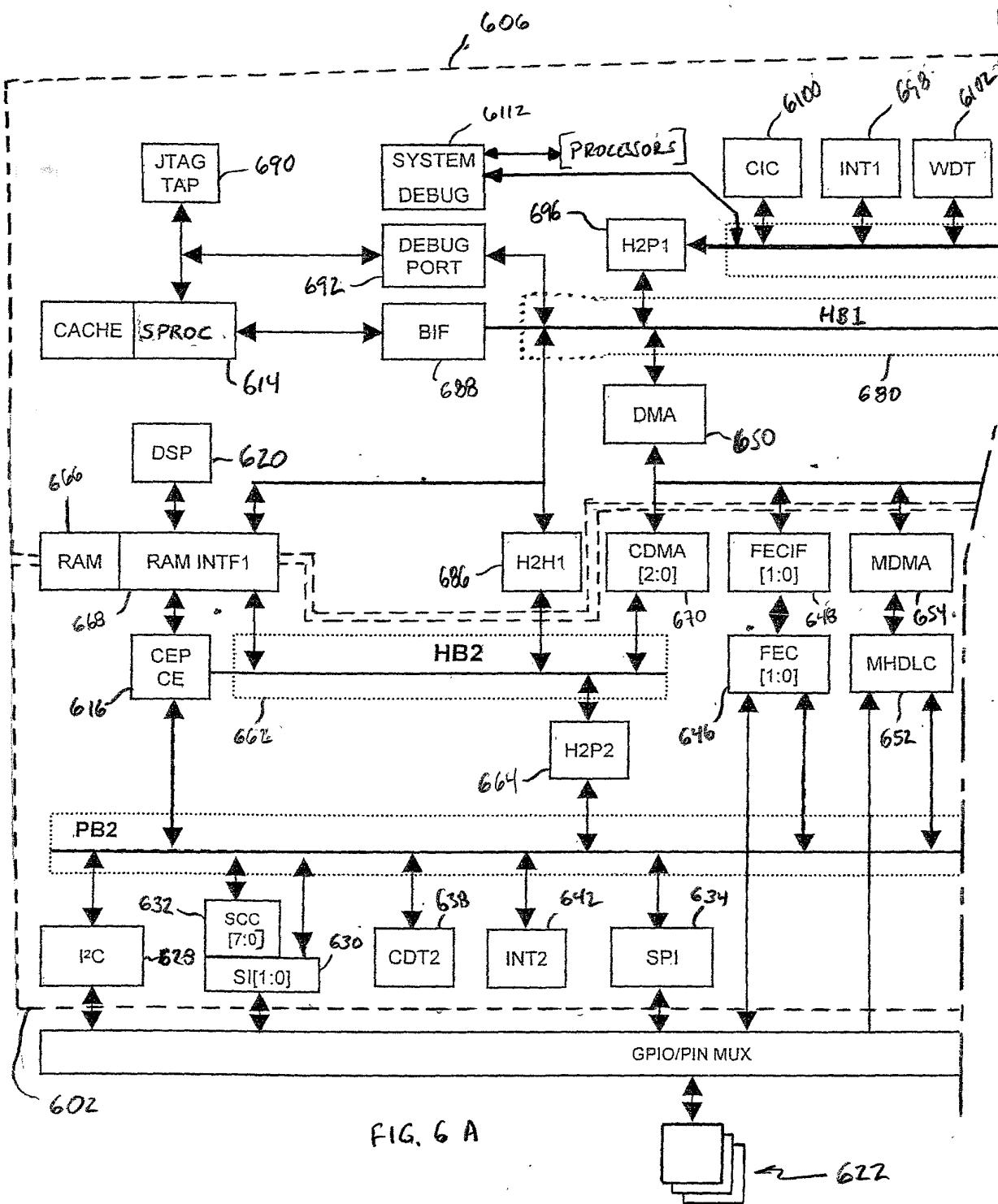


FIG. 4

| Destination Address | | Source Address | |
|---------------------|-----------|---|-----------|
| Register | IW[23:20] | Register Name | Register |
| | | | IW[19:16] |
| R0 | 0000 | GP Register 0 | R0 |
| R1 | 0001 | GP Register 1 | R1 |
| R2 | 0010 | GP Register 2 | R2 |
| R3 | 0011 | GP Register 3 | R3 |
| R4 | 0100 | GP Register 4 | R4 |
| R5 | 0101 | GP Register 5 | R5 |
| R6 | 0110 | GP Register 6 | R6 |
| R7 | 0111 | GP Register 7 | R7 |
| R8 | 1000 | Reserved | R8 |
| R9 | 1001 | Reserved | R9 |
| R10 | 1010 | Pipeline | R10 |
| R11 | 1011 | Stack Pointer | R11 |
| R12 | 1100 | Command | R12 |
| R13 | 1101 | CRC Data/ Addr Compression Data | R13 |
| R14 | 1110 | CRC Remainder Data | R14 |
| R15 | 1111 | CRC Polynomial Addr Compression Mask | IMM |

FIG. 5



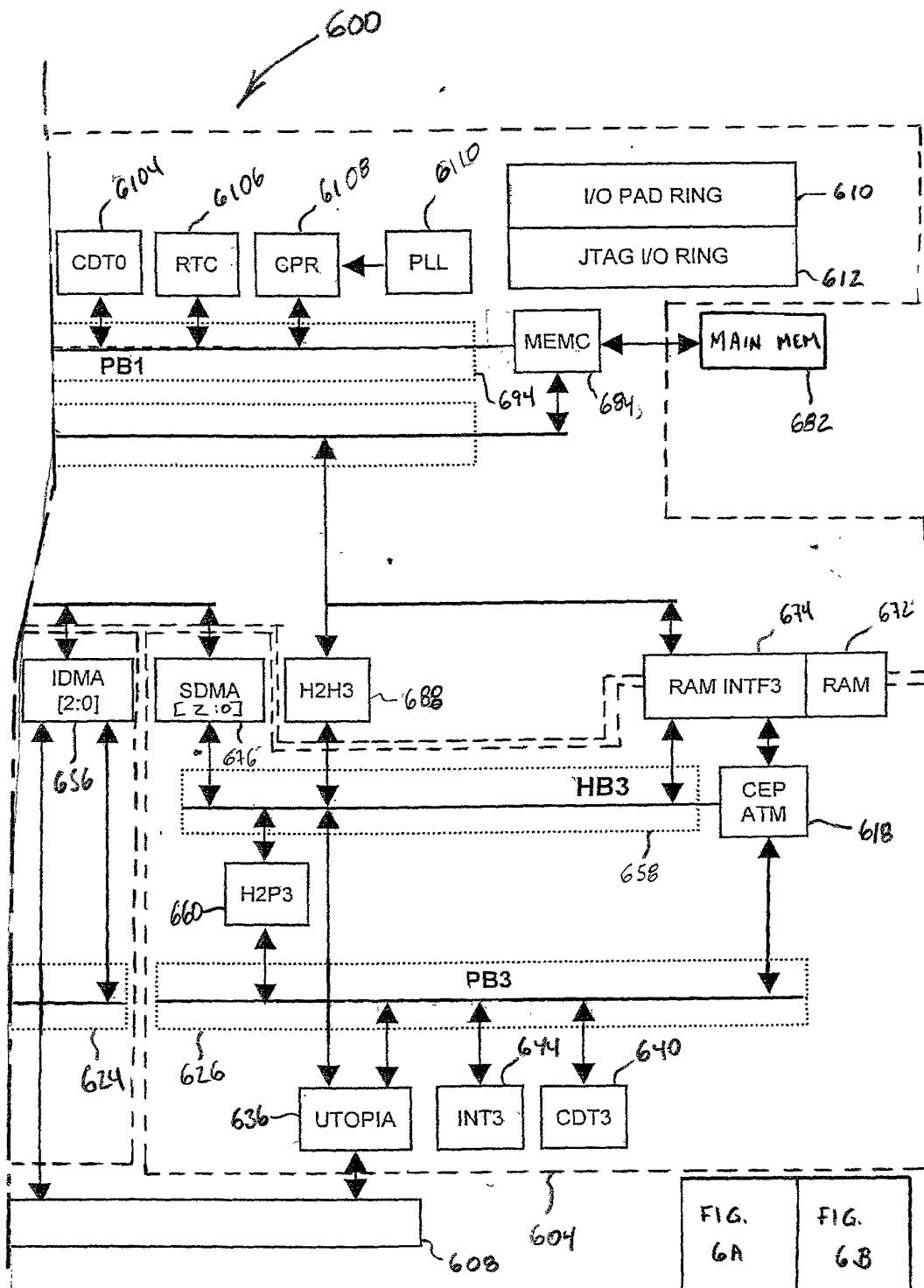


FIG. 6B

FIG. 6